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Date: October 16, 2001 Attorney's Docket No. S1022/8762



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

S1022/8762

Serial No.: Filing Date:

Unassigned Unassigned

For:

ON-CHIP EMULATOR COMMUNICATION

Examiner:

Unassigned

Art Unit:

Unassigned

Box Patent Application Commissioner for Patents Washington, D.C. 20231

STATEMENT FILED PURSUANT TO THE DUTY OF DISCLOSURE UNDER 37 CFR §1.56, 1.97 AND 1.98

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. §1.56, 1.97 and 1.98, the Applicant requests consideration of this Information Disclosure Statement.

Compliance with 37 C.F.R. §1.97

This Information Disclosure Statement is being filed concurrently with the filing of a National Application. No fee or certification is required.

Information Cited

The Applicant hereby makes of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references.

The Applicant hereby makes the following additional information of record in the above-identified application: The examiner's attention is hereby drawn to the attached Standard Search Report.

The applicant would like to bring to the Examiner's attention the following co-pending applications that may contain subject matter related to this application:

Serial No.	Filing Date	<u>Title of Application</u>		
Unassigned	10/16/01	INTERFACE DEVICE		
Unassigned	10/16/01	ON-CHIP EMULATOR COMMUNICATION		

REMARKS

A copy of each of the above-identified information is enclosed unless otherwise indicated on the attached form PTO-1449 (modified). It is respectfully requested that:

Date: October 16, 2001

1. The Examiner consider completely the cited information, along with any other

information, in reaching a determination concerning the patentability of the present claims;

2. The enclosed form PTO-1449 be signed by the Examiner to evidence that the cited

information has been fully considered by the Patent and Trademark Office during the

examination of this application;

3. The citations for the information be printed on any patent which issues from this

application.

By submitting this Information Disclosure Statement, the Applicant(s) makes no

representation that a search has been performed, of the extent of any search performed, or that

more relevant information does not exist.

By submitting this Information Disclosure Statement, the Applicant(s) makes no

representation that the information cited in the Statement is, or is considered to be, material to

patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicant(s) makes no

representation that the information cited in the Statement is, or is considered to be, in fact, prior

art as defined by 35 U.S.C. §102.

Notwithstanding any statements by the Applicant(s), the Examiner is urged to form his or

her own conclusion regarding the relevance of the cited information.

An early and favorable action is hereby requested.

Respectfully submitted,

James H/Morris

Registration No. 34,681

WOLF, GREENFIELD & SACKS, P.C.

600 Atlantic Avenue

Boston, MA 02210

Tel. (617)720-3500

Attorneys for the Applicant(s)

Attorney's Docket No. S1022/8762

Dated: October 16, 2001

FORM PTO-1449/A and B (Modified) INFORMATION DISCLOSURE				A	APPLICATION NO.: ATTY. DOC		CKET NO.: \$1022/8	762	EKET NO.: S1022/8762	
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		Standard Search Report from RS 105936								
		BDM/JTAG Debuggers, Lauterback Technical Manuals, August 9, 2000, XP002161412								
		New Debugger for Direct connection to USB, Lauterback press releases, January 2, 1999, XP002161413								
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